



U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10064398	07/10/2002	726 324	726	2050	KERLERO
**APPLICANTS: Forlenza Donato; Motika Franco; Nigh Phillip;					
**CONTINUING DATA VERIFIED: NONE Jr 6/22/04					
** FOREIGN APPLICATIONS VERIFIED: NONE Jr 6/22/04					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		BUR920010133US1	
Verified and Acknowledged Examiners's initials		Jr 6/22/04			
TITLE : Functional pattern logic diagnostic method					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

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Part Available Copy

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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